


Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1427		SERIAL NO. 09/559,903	
 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Zhiping Yin et al.			
				FILING DATE April 26, 2000		GROUP 2815	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
/PB/	AA	6,133,618	10-2000	Steiner			
/PB/	AB	5,994,217	11-1999	Ng			
/PB/	AC	5,985,519	11-1999	Kakamu et al.			
/PB/	AD	5,747,388	05-1998	Küstners et al.			
/PB/	AE	5,639,687	06-1997	Roman et al.			
/PB/	AF	5,498,555	03-1996	Lin			
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
/PB/	A R		Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pps. 407-413. 1986 Lattice Press				
/PB/	AS		Wolf, S., "Silicon Processing for the VLSI Era", Vol. 2, pgs. 48-49 and 435. 1986 Lattice Press				
EXAMINER				DATE CONSIDERED			
/Paul Budd/				05/15/2009			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							